

**PRELIMINARY AMENDMENT FOR CONTINUING APPLICATION
(CONTINUATION, DIVISIONAL, OR CONTINUATION-IN-PART)**

First Named Inventor Sanjay Dabral

Title AN INLINE AND "Y" INPUT-OUTPUT BUS TOPOLOGY

Attorney Docket No. 42390.P6202D3

Parent Application:

First Named Inventor Sanjay Dabral

Title AN INLINE AND "Y" INPUT-OUTPUT BUS TOPOLOGY

Application No. 09/474,345

Examiner: _____

Filing Date December 29, 1999

Group Art Unit: 2835

Applicant(s): If the present patent application is a Continuation, Divisional, or Continuation-In-Part application under 37 CFR § 53(b) of a parent application, then submit this Preliminary Amendment to the PTO. Please insert the appropriate claim for priority, listing the entire chain of priority.

**To: Assistant Commissioner for Patents
Washington, D.C. 20231**

Sir:

Please enter the following preliminary amendment for the present patent application.

IN THE SPECIFICATION:

At page 1, line _____, please insert the following:

-- The present patent application is a

____ Continuation X Divisional ____ Continuation-in-part (CIP)

of prior application no. 09/474,345, filed December 29, 1999,

entitled AN INLINE AND "Y" INPUT-OUTPUT BUS TOPOLOGY

which is a ____ continuation/ ____ divisional/ ____ CIP of prior application no. _____,

which is a ____ continuation/ ____ divisional/ ____ CIP of prior application no. _____.

8-21-01

Date

Seth Z. Kalson

Signature

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Dabral, et al.

Examiner: **To be assigned**

Application No.: **To be assigned**

Art Unit: **To be assigned**

Filed: **21 August 2001**

For: **An Inline and Y Input-Output Bus
Topology**

Assistant Commissioner for Patents
Washington, D.C. 20231

Preliminary Amendment

Assistant Commissioner for Patents:

Preliminary to an examination on the merits, please amend the above-identified patent application as indicated below.

In the Specification

Please amend the paragraph on page 4, lines 9-18, to read as follows:

In general, for the embodiment of Fig. 4b and other embodiments, vias **408** define a regular array. The region between two consecutive rows (or columns) of a regular array of vias defines a channel. For the embodiment of Fig. 4b and other embodiments, bus lines are routed so as to be within or underneath one and only one channel. That is, in an area or region of a board directly underneath an agent that is to be connected to a bus, individual bus traces making up the bus do not cross from one channel to the next. For the particular embodiment shown in Fig. 4b, bus

traces 410 and 412 are in one channel, and bus traces 414 and 416 are in another channel. Bus topologies such as those according to the embodiments of Figs. 3, 4a, and 4b allow for relatively small stub lengths, and are found to address some or all of the problems cited in the Background.

In the Claims

Please cancel without prejudice claims 1-13.

Remarks

Claim 14 is presently active, claims 1-13 having been cancelled without prejudice by this preliminary amendment.

The patent application filed herewith is a divisional of parent application no. 09/474,345. In the office action dated 11 July 2001 for the parent application (Office Action), claims 1-14 were subject to restriction. Specifically, four species were identified in the Office Action, corresponding, respectively, to: (i) Figs. 4a and b; (ii) Figs. 5a and b; (iii) Fig. 6; and (iv) Fig. 7.

Applicants submit that claims 1-14 are grouped as follows: Claims 1-5 are directed to the species corresponding to Figs. 4a and b; claims 6-8 are directed to the species corresponding to Figs. 5a and b; claims 9-13 are directed to the species corresponding to Fig. 6; and claim 14 is directed to the species corresponding to Fig. 7.

By canceling claims 1-13 by this preliminary amendment, Applicants are electing to prosecute claims directed to the species of Fig. 7.

Minor informalities are corrected in the specification by this Amendment.

Respectfully submitted,

Seth Z Kalan Dated: 8-21-01

Seth Z. Kalson

Reg. no. 40,670

Attorney for Applicants and Intel Corporation (Assignee)

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Amended Version of Specification Showing Changes

(Paragraph on page 4, lines 9-18.)

In general, for the embodiment of Fig. 4b and other embodiments, vias **408** define a regular array. The region between two consecutive rows (or columns) of a regular array of vias [define] defines a channel. For the embodiment of Fig. 4b and other embodiments, bus lines are routed so as to be within or underneath one and only one channel. That is, in an area or region of a board directly underneath [a] an agent that is to be connected to a bus, individual bus traces making up the bus do not cross from one channel to the next. For the particular embodiment shown in Fig. 4b, bus traces **410** and **412** are in one channel, and bus traces **414** and **416** are in another channel. Bus topologies such as those according to the embodiments of Figs. 3, 4a, and 4b allow for relatively small stub lengths, and are found to address some or all of the problems cited in the Background.

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